

Form PTO-1449 OF COMMERCE OFFICE	U.S. DEPARTMENT PATENT AND TRADEMARK INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. YOR9-2000-0402US1 (8728-497)	SERIAL NO. Unassigned J1036 U.S. PTO 09/836375 04/17/01
		APPLICANT Peter W. Cook, Stanley E. Schuster	
		FILING DATE Concurrently herewith	GROUP Art Unit

U.S. PATENT DOCUMENT

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SKS	4 8 3 7 7 4 0	6/6/89	Sutherland			
SKS	5 1 8 7 8 0 0	2/16/93	Sutherland			
SKS	6 1 8 2 2 3 3	1/30/01	Schuster et al.			

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
					YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

SKS	1. Schuster et al., Provisional Application No. 60/212,000 filed June 16, 2000; "Simple Latch Structure for Interlocked Pipelined CMOS (IPCMOS) Circuits"
SKS	2. Schuster et al., "Asynchronous Interlocked Pipelined CMOS Circuits Operating at 3.3-4.5GHz." OSSCC 2000/Session 17/ Logic and Systems/Paper WA 17.3
SKS	3. Cook et al., U.S. Patent Application Serial No. 09/746,647 filed December 21, 2000; "Asynchronous Pipelined Control Interface"

EXAMINER Suresh K Surogawanshi DATE CONSIDERED 3/29/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.